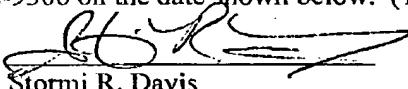


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I hereby certify that the attached Transmittal (1 page) with Response to the Restriction Requirement dated January 21, 2005 (1 page) are submitted to the U.S. Patent and Trademark Office via facsimile number (703) 872-9306 on the date shown below. (Total 2 pages).



Stormi R. Davis

Date: February 18, 2005

PATENT APPLICATION  
Attorney's Doc. No.: 4591-342  
Client Ref. No.: ID12015-US

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: Kyu-Chan LEE, et al.

Serial No.: 10/621,165 Examiner: Nguyen, Viet Q.

Filed: July 15, 2003 Group Art Unit: 2818

Confirmation No.: 4320

For: SEMICONDUCTOR MEMORY DEVICE HAVING AN  
INTERNAL VOLTAGE GENERATION CIRCUIT FOR  
SELECTIVELY GENERATING AN INTERNAL VOLTAGE  
ACCORDING TO AN EXTERNAL VOLTAGE LEVEL

TRANSMITTAL LETTER

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Enclosed for filing in the above-referenced application are the following:

- Response to Restriction Requirement
- Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,  
MARGER JOHNSON & McCOLLOM, P.C.



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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**RESPONSE TO RESTRICTION REQUIREMENT**Responsive to the Restriction Requirement dated January 21, 2005, Applicant hereby  
elects Group I (claims 1-11), without traverse.**Customer No. 20575**

Respectfully submitted,

MARGER JOHNSON &amp; McCOLLOM, P.C.

  
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 Stormi R. Davis